

In re Patent Application of:  
**SANCHES ET AL.**  
Serial No. 09/915,761  
Filing Date: JULY 26, 2001

In the Title:

Please replace the title with the following  
rewritten title:

SIGNAL PROCESSOR EXECUTING VARIABLE SIZE  
INSTRUCTIONS USING PARALLEL MEMORY BANKS THAT DO NOT INCLUDE  
ANY NO-OPERATION TYPE CODES, AND CORRESPONDING METHOD